

FIG. 1

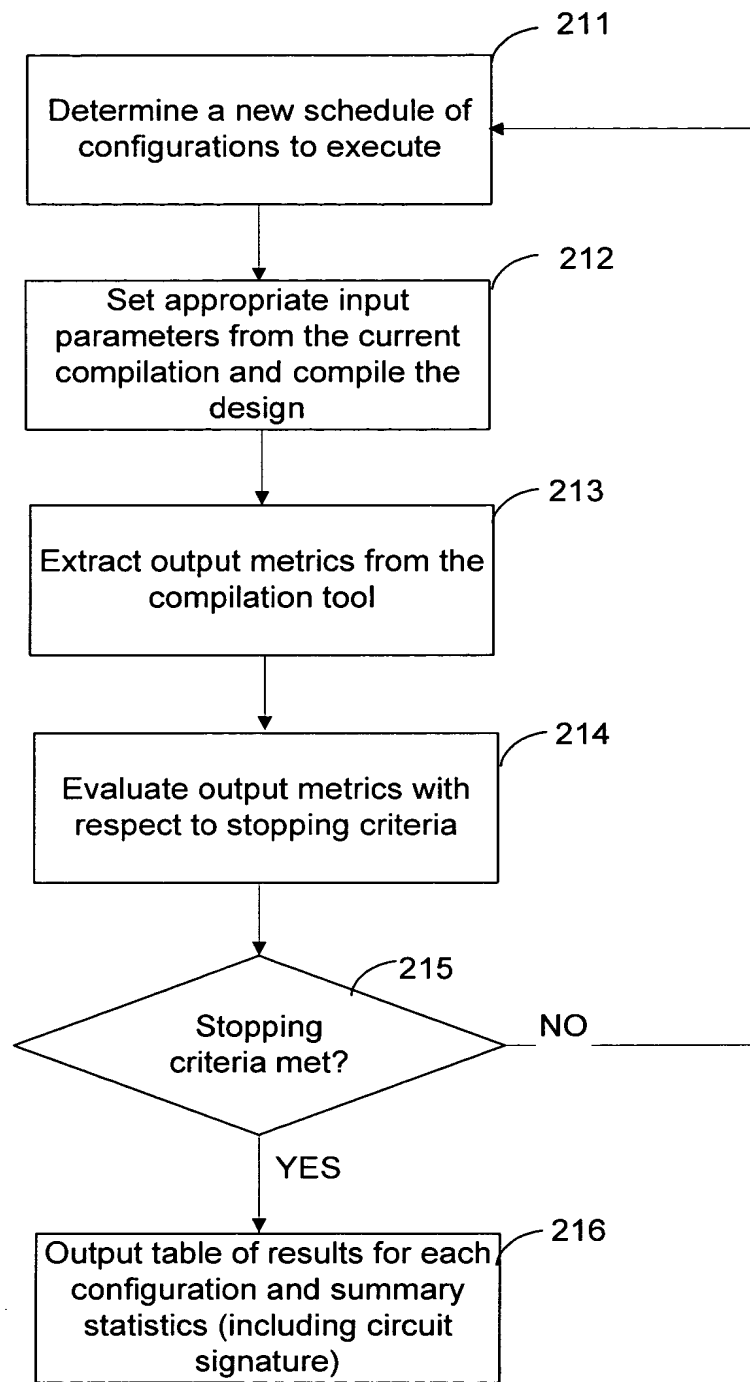


FIG. 2

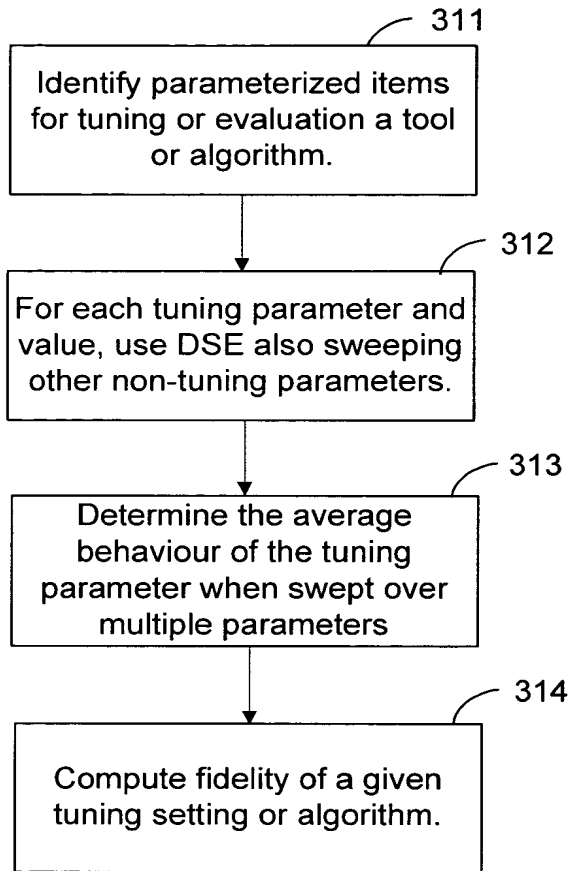


FIG. 3a

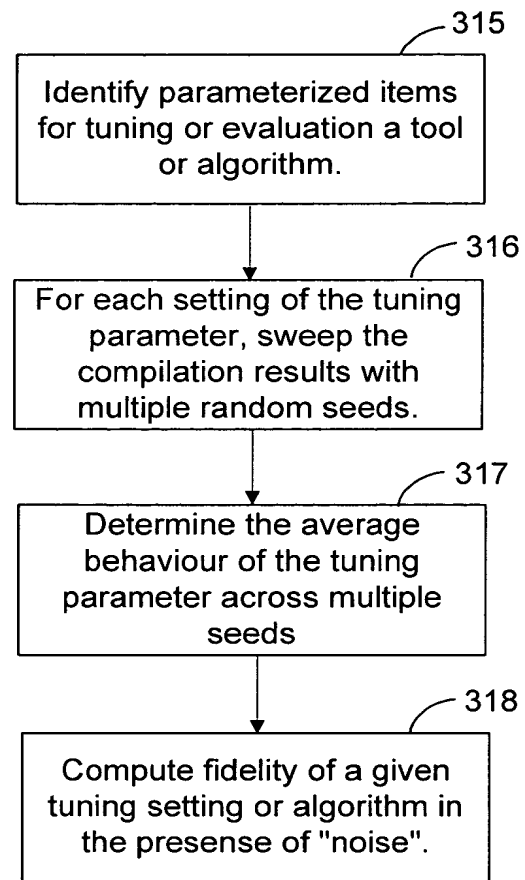


FIG. 3b

400

401

Project and Seed Settings:

Project: Simple

Seeds to Sweep: 1 5 7 11-13 23

404

Compilation Output Data Displayed Here

402

Exploration Type:

☒ Type 1: Seed sweeping, register packing, and making the fitter try harder

☐ Type 2: Type 1 plus LE duplication, LUT resynthesis and WYSIWYG remapping

☐ Type 3: Type 2 plus register retiming

☐ Custom Exploration DEFINE

405

Best Results:

Period: 10 ns

Slack: -288 ns

Failing Paths: 2

Compile Time: 50s

Logic cell count: 5

403

Exploration Options:

Stop exploration after this much time has elapsed 1 Hours

Stop exploration after this gain in period is achieved 30 %

406

☒ Archive each compile

☒ Show compilation results

Start Stop

FIG. 4

